

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	739	(boot-up or boot\$4 same (NVRAM or non-volatile) same (mcu or microcontroller or micro) same (time or timing or times or timed)) and (access same (record or information or file)) and (login or logged or logging or log-in)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/03 14:34
L2	736	(boot-up or boot\$4 same (NVRAM or non-volatile) same (mcu or microcontroller or micro) same (time or timing or times or timed) same (host or server)) and (access same (record or information or file)) and (login or logged or logging or log-in)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/03 14:35
L3	247	(boot-up or boot\$4 same (NVRAM or non-volatile) same (mcu or microcontroller or micro) same (time or timing or times or timed) same (host or server)) and (access same (record or information or file)) and (login or logged or logging or log-in) and (backup or NAS)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/03 14:36
L4	244	(boot-up or boot\$4 same (NVRAM or non-volatile) same (mcu or microcontroller or micro) same (time or timing or times or timed) same (host or server)) and (access same (record or information or file)) and (login or logged or logging or log-in) and (backup or NAS) and (network or internet or ethernet)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/03 14:36

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	19	(chang-cheng-han.in. or cheng-tsun-chin.in. or liu-chien-hsing.in.)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/03 13:45

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	4	(707/10.ccls. or 707/200.ccls. or 707/204.ccls. or 707/2.ccls. or 714/15.ccls. or 714/18.ccls. or 714/9.ccls. or 709/224.ccls. or 709/225.ccls. or 709/223.ccls. or 709/229.ccls. or 709/203.ccls. or 709/217.ccls. or 709/219.ccls. or 713/1.ccls. or 713/2.ccls. or 713/182.ccls. or 713/185.ccls. or 710/36.ccls. or 710/39.ccls. or 710/45.ccls.) and ((microcontroller or mcu or micro or micro-controller) same (boot-up or boot\$4) same tim\$4 same (nonvolatile or non-volatile or NVRAM)) and (log\$3 same tim\$4) and (access same (record or file or information))	US-PGPUB; USPAT; USOCR	OR	ON	2007/03/03 16:32
L2	3	(702/119.ccls. or 702/123.ccls. or 702/122.ccls. or 702/182.ccls. or 702/183.ccls. or 702/188.ccls. or 711/100.ccls. or 711/111.ccls. or 711/112.ccls. or 711/104.ccls. or 711/154.ccls. or 711/163.ccls. or 711/167.ccls. or 711/168.ccls. or 711/155.ccls. or 711/150.ccls. or 711/151.ccls. or 711/162.ccls.) and ((microcontroller or mcu or micro or micro-controller) same (boot-up or boot\$4) same tim\$4 same (nonvolatile or non-volatile or NVRAM)) and (log\$3 same tim\$4) and (access same (record or file or information))	US-PGPUB; USPAT; USOCR	OR	ON	2007/03/03 16:35



☐ Search Session History

Edit an existing query or
compose a new query in the
Search Query Display.

Select a search number (#)

to:

- Add a query to the Search Query Display
- Combine search queries using AND, OR, or NOT
- Delete a search
- Run a search

Recent Search Queries

#1 ((access<paragraph>(record<or>records)<and>
(mcu<or>microcontroller<or>micro)<and>(non-
volatile<or>nvr–ram)<and>(boot-
up<or>booting<or>boot<or>booted))<in>metadata)

Results

0

Indexed by
Inspeck

[Home](#) | [Login](#) | [Logout](#) | [Access Information](#) | [Alerts](#) | [Sitemap](#) | [Help](#)

Welcome United States Patent and Trademark Office

[BROWSE](#) [SEARCH](#) [IEEE XPLORE GUIDE](#) [SUPPORT](#)

Sat, 3 Mar 2007, 4:02:43 PM EST

Search Query Display



[Help](#) [Contact Us](#) [Privacy & Security](#) [IEEE.org](#)
© Copyright 2006 IEEE – All Rights Reserved